
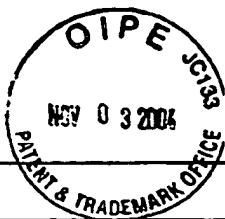


Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DOCKET NO: 5150-61502 <div style="text-align: center;">  </div>		SERIAL NO: 10/058,150			
		APPLICANT: Odom et al.					
		FILING DATE: October 29, 2001		GROUP: 2123			
U.S. PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
FOREIGN PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
IG	D1	DE 692 32 869 T2	Sept. 4, 2003	Germany	 	 	YES
IG	D2	DE 42 05 524 A1	Aug. 27, 1992	Germany	 	 	YES
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
IG	D3	Lauwereins, R; Engels, M; Ade, M; Peperstraette, J; Grape-II: A System-Level Prototyping Environment for DSP Applications, Computer, Vol. 28, Issue 2, pp. 35-43, 2/95.					
IG	D4	Lysaght, P; Stockwood, J; A Simulation Tool for Dynamically Reconfigurable Field Programmable Gate Arrays, IEEE Transactions on Very Large Scale Integration Systems, Vol. 4, Issue 3, pp. 381-390, 9/96.					
IG	D5	Petronino et al., An FPGA-based Data Acquisition System for a 95 GHz. W-band Radar, IEEE International Conference on Acoustics, Speech and Signal Processing, Vol. 5, pp. 4105-4108, April 1997.					
IG	D6	Boulay et al., A High Throughput Controller for a 256-Channel Cardiac Potential Overlapping System, Canadian Conference on Electrical and Computer Engineering, Vol. 1, pp. 539-542, September 1995.					
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10	C1	International Search Report, Application No. PCT/US02/22895, mailed December 9, 2003.					
EXAMINER: <i>102hl</i>			DATE CONSIDERED: <i>3/3/05</i>				
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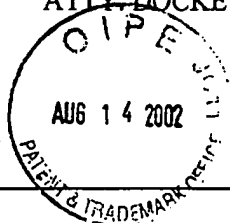
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KG	A1	4,849,880	7/18/89	Bhaskar et al.			
	A2	4,901,221	2/13/90	Kodosky et al.			
	A3	5,109,504	4/28/92	Littleton			
	A4	5,437,464	8/1/95	Terasima et al.			
	A5	5,497,498	3/5/96	Taylor			
	A6	5,535,342	7/9/96	Taylor			
	A7	5,555,201	09/10/1996	Dangelo et al.			
	A8	5,566,295	10/15/96	Cypher et al.			
	A9	5,583,749	12/10/96	Tredennick et al.			
	A10	5,603,043	02/11/1997	Taylor et al.			
	A11	5,603,043	2/11/97	Taylor et al.			
	A12	5,652,875	7/29/97	Taylor			
	A13	5,684,980	11/4/97	Casselman			
	A14	5,732,277	03/24/1998	Kodosky et al.			
	A15	5,784,275	7/21/98	Sojoodi et al.			
	A16	5,847,955	12/98	Mitchell et al.			
	A17	6,219,628	04/2001	Kodosky et al.			
	A18	6,226,776	05/01/2001	Panchul et al.			
	A19	6,230,307	05/082001	Davis et al.			
KG	A20	6,311,149	10/30/2001	Ryan et al.			


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KG	A21	WO 94 10627 A	5/11/94	PCT			
	A22	WO 94 15311 A	7/7/94	PCT			
	A23	WO 96 14618 A	5/17/96	PCT			

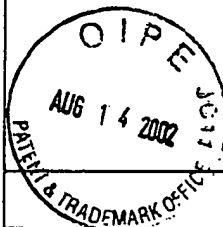
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13	A24	Douglas, Bruce D., Ph.D., "Custom Embedded Communication Protocols, Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 637-657.	
	A25	Labrosse, Jean, "Inside Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 205-214.	
	A26	Labrosse, Jean, "Designing with Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 379-389.	
	A27	Operating Systems, 2 nd Ed. By Williams Stallings, Prentice Hall, 1995, "Multiprocessor and Real-Time Scheduling", pp. 394-415.	
	A28	International Search Report for PCT/US 98/10916 dated 9/17/98.	
	A29	"LabVIEW USER MANUAL for Windows", National Instruments Co., pp. 4/1-21.	
	A30	Beguelin et al., "Visualization and Debugging in a Heterogenous Environment," IEEE, pp. 88-95.	
	A31	Jamal et al., "The Applicability of the Visual Programming Language LabView to Large Real-World Application," IEEE, pp. 99-106.	
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	A41	Spoelder et al., "Virtual Instrumentation: A Survey of Standards and Their Interrelation", Proc. IEEE Instr. And Measurement Tech. Conf., Vol. 1, pp. 676-681, May 1997.
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	A44	Fountain: "Software advances in measurement and instrumentation"; IEE Coll. Software Instrumentation; pp. 4/1 to 4/45.

EXAMINER:

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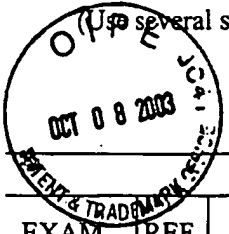
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		Technology Center 2100					
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OS	B0	6,219,628	04/17/2001	Kodosky et al.			
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EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
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	B2	Real-Time Workshop for Use with Simulink, User's Guide, May 1994, 229 pages.					
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	B4	Real-Time Interface to Simulink, RTI 30, User's Guide, 1995, 125 pages.					
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	B6	SPW - MatLAB Co-Simulation Interface Product Data Sheet, 1996, 2 pages.					
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	B10	SPW/CGS Porting Kits Product Data Sheet, 11/94, 2 pages.					
	B11	MultiProx for SPW Product Data Sheet, 08/94, 4 pages.					
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	B14	Available XA-1000 Literature and Its Use, 1986, 2 pages.					
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For Applicant's Information *

Disclosure Statement

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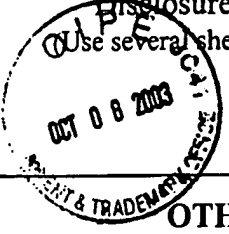
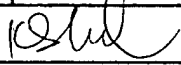
APPLICANT: Odom et al.

FILING DATE: October 29, 2001

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B21	Xanalog Real-Time User Guide, 1994, 28 pages.
B22	Lee et al., "Gabriel: A Design Environment for Programmable DSPs", 11/7/1988, 13 pages.
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B35	Vol. 1 - Ptolemy 0.7 User's Manual, 1997, 532 pages. —
B36	i-Logix Product Overview, 1996, 52 pages.
B37	Press Release, i-Logix Statemate MAGNUM Supports PCs", 01/31/1997, 2 pages.
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B44	International Search Report, Application No. PCT/US02/22895, mailed September 18, 2003.

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B45	Wenban et al. "A Software Development System for FPGA-Based Data Acquisition Systems, 1996, pgs. 28-37.	
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